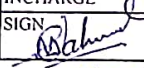
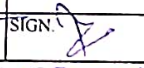
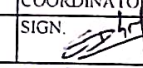
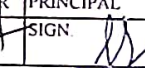
		JCEI'S JAINHIND COLLEGE OF ENGINEERING, KURAN Affiliated to Savitribai Phule Pune University, Pune AN ISO 14001:2004 EMS & An ISO:9001:2015 QMS CERTIFIED INSTITUTE			TIME TABLE (ACADEMIC YEAR -2023-24)		
CLASS :- S.E.		DIV:	SEMESTER: I		W.E.F. :- DATE: 10/08/2023		
NAME OF CLASS TEACHER:- Prof. Gadekar C. B.					BLOCK / CLASS NO:-10		
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09.15-10:15	DS	DS	ELC	DC	DS	DC	
10:15-11:15	EXC	EXC	EXC	EXC	ELC	M-III	
11:15-12:00	RECESS						
12.00-01:00	DC	DC	DS	ELC	M-III TUT	DS S3 ESD S2 Library S1	
01.00-02:00	M-III	ELC	M-III	M-III	Library		
2.00-2:15	RECESS						
2.15-3:15	ESD S1	EXC S1	DS S1	DC -S1	ELC S1	MAC 3	
3.15-4:15	DS S2 Library- S3	ELC S2 DC- S3	ESD S3 Library S2	EXC S2 ELC S3	DC -S2 EXC S3		
Batches :- S1 :- 1-25 S2- 26-50 S3- 51 to onwards							
Name Of Subject	TH/PR/OR/TW	Abbreviation	Subject Code	CLASS/LAB NO.	Name of Subject Teacher	Guardian for Roll Nos.	
Engineering Maths-III	TH/TW	MIII	207005	S010	Prof. Doko K.		
Electronic Circuits	TH	EXC	204181	S010	Dr. Muljkar R.M.		
Digital Circuits	TH	DC	204182	S010	Prof. Gadekar C. B.		
Electrical Cicuits	TH	ELC	204183	S010	Prof. Rahane S. L.		
Data Structure	TH	DS	204184	S010	Prof. Raut P. K.		
Electronic Circuits Lab	PR	EXC Lab	204185	S002	Prof. Chaugule Sw. D.		
Digital Circuits Lab	PR	DC Lab	204186	S001	Prof. Gadekar C. B.		
Electrical Circuits Lab	TW	ELC Lab	204187	G001	Prof. Rahane S. L.		
Data Structure Lab	OR	DS Lab	204188	S022	Prof. Raut P. K.		
Electronic Skill Development	TW	ESD	204189	S007	Prof. Satpute R. S. Prof. Chaugule Sw. D. Prof. Chaugule S. D.		
Mandatory Audit Course 3		MAC-3	204190	S007	Prof. Gadekar C. B.		
ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY	PAGE No	CHAPTER No.	
No. : 00	No. : 00	DSGN: T T INCHARGE	DSGN: HOD	DSGN: IQAC COORDINATOR	DSGN: PRINCIPAL		
DATE: 10/08/2023	DATE: 10/08/2023	SIGN: 	SIGN: 	SIGN: 	SIGN: 	01 OF 01	
F/JCOE/AC/01/00/25/07/2022		Head of Department (E & TC)		Principal			


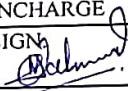
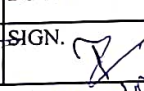
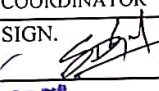
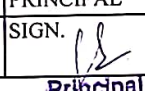
JCEI's Jainhind College Of Engineering
 Jainhind College of Engineering Kuran, Tal. Junnar, Dist. Pune - 410511
 Tal. Junnar, Dist. Pune

JCEI'S JAIHIND COLLEGE OF ENGINEERING, KURAN Affiliated to Savitribai Phule Pune University, Pune AN ISO 14001:2004 EMS & An ISO:9001:2015 QMS CERTIFIED INSTITUTE.					TIME TABLE (ACADEMIC YEAR -2023- 24)		
CLASS :- T.E.					DEPARTMENT:- Electronics and Telecommunication Engineering		
NAME OF CLASS TEACHER:-Prof. Satpute R. S.		DIV:	SEMESTER:I		W.E.F. :- DATE:10/07/2023		
					BLOCK / CLASS NO:- 11		
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09.15-10:15	MC	EFT	EFT	EFT	JAVA	EFT	
10:15-11:15	DC	MC	DBM	DBM	MC	MC	
11:15-12:00	RECESS						
12.00-01:00	DBM-T1	DC-T1	SD-T1	JAVA-T1	MC-T1	JAVA	
01.00-02:00	MC-T2	DBM-T2	MAC 5 - T2	DC-T2	JAVA-T2	DC	
2.00-2:15	RECESS						
2.15-3:15	JAVA	DC	DC	DBM	SD- T2	DBM	
3.15-4:15	LIBRARY	EFT-TUT(T1/T2)	JAVA	DC	MAC 5 - T1	LIBRARY	
Batches :- T1 :-01 To 19 T2-20 to 37							
Name Of Subject	TH/PR/OR/TW	Abbreviation	Subject Code	CLASS /LAB No.	Name of Subject Teacher	Guardian for Roll Nos.	
Digital Communication	TH	DC	304181	S011	Prof. Chaugule S. D.		
Electromagnetics field theory	TH	EFT	304182	S011	Dr. Mulajkar R. M.		
Database Managemet	TH	DBM	304183	S011	Prof.Raut P.K.		
Microcontrollers	TH	MC	304184	S011	Prof. Nalawade M. S.		
EL-I (FUNDAMENTAL OF JAVA)	TH	JAVA	304185	S011	Prof. Satpute R. S		
Digital Communication Lab	PR	DC Lab	304186	S002	Prof. Chaugule S. D.		
Database Managemet Lab	PR	DBM Lab	304187	S002	Prof.Raut P.K.		
Microcontrollers Lab	PR	MC Lab	304188	S007	Prof. Nalawade M. S.		
EL-I (JAVA) Lab	PR	Java Lab	304189	S020	Prof. Satpute R. S		
Skill Development	PR	SD Lab	304190	S007	Prof. Nalawade M. S.		
Mandatory Audit Course 5		MAC-5	304191A	-	Prof. Satpute R. S		
ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY		PAGE No	CHAPTER No.
No. : 00	No. : 01	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: IQAC COORDINATOR	DSGN.: PRINCIPAL	01 OF 01	LIST-JCOE-01
DATE::10/07/2023	DATE:10/07/2023	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>		

F/JCOE/AC/01/00/25/07/2022

Head of Department
(E & TC)
JCEI's Jaihind College of Engineering,
Kuran, Tal-Junnar, Dist. Pune

Principal
JCEI's Jaihind College Of Engineering
Kuran, Tal.Junnar, Dist.Pune - 410511

		JCEI'S JAIHIND COLLEGE OF ENGINEERING, KURAN Affiliated to Savitribai Phule Pune University, Pune AN ISO 14001:2004 EMS & An ISO:9001:2015 QMS CERTIFIED INSTITUTE			TIME TABLE (ACADEMIC YEAR -2023- 24) DEPARTMENT:- Electronics and Telecommunication Engineering		
CLASS :- B.E.		DIV:	SEMESTER: I		W.E.F. :- DATE: 10/07/2023		
NAME OF CLASS TEACHER:- Prof. Chaugule Sonali D.					BLOCK / CLASS NO:- 12		
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09.15-10:15	CC	VLSI	EPD	VLSI	EPD	CC	
10:15-11:15	EPD	RMT	IOT	RMT	IOT	VLSI	
11:15-12:00	RECESS						
12.00-01:00	Library	CC	RMT	PP-I B1/B2	IOT-B1 B2	VLSI- RMT	
01.00-02:00	VLSI	IOT	CC			IOT	
2.00-2:15	RECESS						
2.15-3:15	VLSI-B1	IOT-	CC-B1	PP-I	RMT-B1	CC-	
3.15-4:15	B2		RMT-B2	B1/B2	B2	PP-I B1/B2	
						EPD MAC 7	
Batches :- B1 :-01 To 12 B2-13 to 24							
Name Of Subject	TH/PR/OR/ TW	Abbreviation	Subject Code	CLASS/Lab No.	Name of Subject Teacher		
Radiation & Microwave Techniques	TH	RMT	404181	S012	Dr. Mrs. Dhede V.M		
VLSI Design & Technique	TH	VLSI	404182	S012	Prof. Chaugule S.D..		
Cloud computing	TH	CC	404183	S012	Prof. Satpute R. S		
Modernized IOT	TH	IOT (EL-III)	404184	S012	Prof. Nalawade M.S.		
Electronic Product Design	TH	EPD(EL-IV)	404185	S012	Prof. Chaugule Sw.D.		
Lab Practice-I (RMT+CC)	OR/TW	LP-I	404186	S001+S022	Dr. Mrs. Dhede V.M/Prof. Satpute R. S		
Lab Practice-II (VLSI+IOT(EL-III))	PR/TW	LP-II	404187	S020+S001	Prof. Nalawade M. S./ Prof. Chaugule S. D.		
Project Phase -I	OR	PP-I	404188	S007	Prof. Nalawade M. S./Dr. Mulajkar R. M.		
Audit Course 7	-	AC	404189		Prof. Chaugule S.D..		
ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY		CHAPTER No.	
No. : 00	No. : 01	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: IQAC COORDINATOR	DSGN.: PRINCIPAL	01 OF 01	
DATE: 10/07/2023	DATE: 10/07/2023	SIGN. 	SIGN. 	SIGN. 	SIGN. 	LIST-JCOE-01	

F/JCOE/AC/01/00/25/07/2022

Head of Department
(E & TC)
 JCEI's Jaihind College of Engineering,
 Kuran, Tal. Junnar, Dist. Pune - 410511

Principal
 JCEI's Jaihind College Of Engineering
 Kuran, Tal. Junnar, Dist. Pune - 410511